

**IN THE
UNITED STATES PATENT AND TRADEMARK OFFICE**

INVENTORS: Sabhapathi Naidu Annamaneni and Sarangkumar Sharadchandra Bhavsar

SERIAL NO.: 10/619,738

FILING DATE: July 14, 2003

TITLE: System and Method for Automating Integration of Semiconductor Work in Process Updates

EXAMINER: Carlos R. Ortiz Rodriguez

GROUP ART UNIT: 2125

ATTY. DKT. NO.: 22272-08030

CERTIFICATE OF EFS-WEB TRANSMISSION

Pursuant to 240 OG 45 and the Legal Framework For EFS-Web, I hereby certify that this follow-on correspondence is being officially submitted through the USPTO EFS-Web system from the Pacific Time Zone of the United States on the local date shown below:

Dated: April 15, 2008 By: /Michael W. Farn/
Michael W. Farn, Reg. No.: 41,015

MAIL STOP AMENDMENT
COMMISSIONER FOR PATENTS
P.O. BOX 1450
ALEXANDRIA, VA 22313-1450

AMENDMENT B

Dear Examiner:

This amendment is in response to the Office Action mailed November 16, 2007, which established a shortened statutory period for response that expired on February 16, 2008. A fee for a two-month extension of time is included herewith.

[REMAINDER OF PAGE INTENTIONALLY LEFT BLANK]